



Benha University

Logic Circuits ECE 224C

Computer Systems Engineering
Electrical Engineering Department



Faculty of Engineering
(at Shoubra)

Sheet 3

Solve the following problems from *Digital Design: With an Introduction to the Verilog HDL*:

- 2.1
- 2.4
- 2.9
- 2.14
- 2.16
- 2.18
- 2.20
- 2.21
- 2.22
- 2.24
- 2.26
- 2.27
- 2.28